

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	2	(emulat\$4 and dynamic and (condition\$1 adj code\$1)).clm.	US-PGPUB	OR	ON	2007/05/14 19:12
L11	9	(emulat\$4 and (condition\$1 adj code\$1)).clm.	US-PGPUB	OR	ON	2007/05/14 19:12

EAST Search History

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L2	386	703/26.ccls.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/05/14 17:31

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	195	717/138.ccls.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/05/14 18:48

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	31	emulation and ((runtime or (run adj time)) near translation) and instruction\$1	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/05/14 18:23

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	6	(multiple adj condition adj code) and (single adj condition)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/05/14 18:39

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L12	4	(plurality near (condition\$1 adj code\$1)) and (single adj condition)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/05/14 19:16

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	33	multiple adj condition adj code	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/05/02 14:59

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	36	hitachi.as. and @ad<"20020101" and @ad>"19960101" and (condition adj code\$1)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/05/14 19:00


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multiple condition codes

1950

- 2001

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All Results[R Lupas](#)[M Varanasi](#)[P Mead](#)[S Verdu](#)[B Aazhang](#)
[Code division **multiple**-access techniques in optical fiber networks. I.](#)
[Fundamental principles - all 5 versions »](#)

 JA Salehi, BC Res, NJ Morristown - Communications, IEEE Transactions on, 1989 -
 ieeexplore.ieee.org

 ... processing, namely, the optical orthogonal **codes**" that satisfy the above two **conditions**
 is introduced ... study of OOC's in a **multiple**-access environment ...

[Cited by 426](#) - [Related Articles](#) - [Web Search](#)
[Linear multiuser detectors for synchronous code-division **multiple**-access
 channels - all 7 versions »](#)

R Lupas, S Verdu - IEEE Transactions on Information Theory, 1989 - princeton.edu

 ... Abstract -In **code**-division **multiple**-access systems ... the optimum linear detector is
 obtained subsequently, along with sufficient **conditions** on the signal ...

[Cited by 958](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)
[Computer processor with an efficient means of executing many instructions
 simultaneously - all 3 versions »](#)

MH Branigin - US Patent 5,471,593, 1995 - Google Patents

 ... instruction was successful or unsuccessful in setting the **condition code** and to
 conditionally execute accordingly; by **multiple condition codes** so that indepen ...

[Cited by 83](#) - [Related Articles](#) - [Web Search](#)
[Processor architecture supporting **multiple** speculative branching - all 2
 versions »](#)

 V Popescu, MA Schultz, GA Gibson, JE Spracklen, BD ... - US Patent 5,561,776, 1996 -
 Google Patents

 ... 11 is a schematic diagram of the **condition code** and branch shelf circuitry; and FIG ...
 a diagram illustrating the system for assigning instructions to **multiple** ALU's ...

[Cited by 56](#) - [Related Articles](#) - [Web Search](#)
[... order execution, speculative branching, and giving priority to instructions
 which affect a **condition** ... - all 3 versions »](#)

 V Popescu, MA Schultz, GA Gibson, JE Spracklen, BD ... - US Patent 5,625,837, 1997 -
 Google Patents

 ... EXECUTION, performance is even more pronounced when **multiple** ...
 pipelined processor design,

 putting N pipelines in parallel so AFFECT A **CONDITION CODE** 5N instructions can ...

[Cited by 55](#) - [Related Articles](#) - [Web Search](#)
[Very low rate convolution **codes** for maximum theoretical performance of
 spread-spectrum **multiple**- ... - all 5 versions »](#)

 AJ Viterbi, Q Inc, CA San Diego - Selected Areas in Communications, IEEE Journal on, 1990
 - ieeexplore.ieee.org

 ... SSMA), sometimes also called **code** division **multiple** ac- bol ... latter **condition** also
 justifies the truncation of the ... use of very low rate **codes** practically always ...

[Cited by 340](#) - [Related Articles](#) - [Web Search](#)



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"multiple condition codes"

1950

- 2001

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[B Blaner](#)

[A Mohamed](#)

[L Larsen](#)

[D Weaver](#)

[M Branigin](#)

[Multiple condition code branching system in a multi-processor environment - all 3 versions »](#)

B Blaner, LD Larsen - US Patent 5,659,722, 1997 - Google Patents

... inven- accordance with the nt mventi on. ùon allows **multiple**

condition codes to be combined into „_ _

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[Computer processor with an efficient means of executing many instructions simultaneously - all 3 versions »](#)

MH Branigin - US Patent 5,471,593, 1995 - Google Patents

... instruction was successful or unsuccessful in setting the condition code and to conditionally execute accordingly; by **multiple condition codes** so that indepen ...

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[\[book\] The SPARC Architecture Manual - all 81 versions »](#)

DL Weaver, T Germond - 1994 - scitechsoft.com

Page 1. The SPARC Architecture Manual Version 9 SPARC International, Inc. San Jose, California David L. Weaver / Tom Germond Editors SAV09R1459912 ...

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[Partners in platform design - all 4 versions »](#)

M Tremblay, P Tirumalai - Spectrum, IEEE, 1995 - ieeexplore.ieee.org

... In addition, a few processors support the speculative loading of data (nonfaulting loads), **multiple condition codes**, and sophisticated branch predictioo. ...

Cited by 6 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[... system adapted for single instruction branching on multiple condition codes from plural processing ... - all 2 versions »](#)

B BLANER, LD LARSEN - EP Patent 0,682,309, 1995 - v3.espacenet.com

Data processing system adapted for single instruction branching on **multiple condition codes** from plural processing elements. ...

[Cached](#) - [Web Search](#)

[THE MULTISCALAR ARCHITECTURE - all 12 versions »](#)

M Franklin - 1993 - nano.cc.gatech.edu

Page 1. THE MULTISCALAR ARCHITECTURE by MANOJ FRANKLIN A thesis submitted in partial fulfillment of the requirements for the degree of ...

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[Sun Microsystems Laboratories The First Ten Years 1991- 2001 - all 9 versions »](#)

J Treichel, M Holzer - Perspectives, 2001 - sun.com

... of having many integer condition codes, so we get the same advantages of parallelism on the integer side that we got from **multiple condition codes** on the ...

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B Blaner, LD Larsen - US Patent 5,659,722, 1997 - Google Patents

... elements to a **single condition code** necessarily discards ... accordance with the nt mventi

on. ùon allows **multiple condition codes** to be combined into ...

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"multiple condition codes" emulation

1950

- 2001

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T Germond

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Sun Microsystems Laboratories The First Ten Years 1991– 2001 - all 9 versions »

J Treichel, M Holzer - Perspectives, 2001 - sun.com

... Having **multiple condition codes** allows more things to go on in parallel. ... The benefit here is that it will give you very fast instruction **emulation**, and very ...

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IEEE JNL IEEE Journal or Magazine
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IEEE CNF IEEE Conference Proceeding
IET CNF IET Conference Proceeding
IEEE STD IEEE Standard

» Key



1. Partners in platform design

Tremblay, M.; Tirumalai, P.;

[Spectrum, IEEE](#)

Volume 32, Issue 4, April 1995 Page(s):20 - 26

Digital Object Identifier 10.1109/6.375994

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2. A DSP architectural design for low bit-rate motion video codec

Murakami, T.; Kamizawa, K.; Kameyama, M.; Nakagawa, S.-I.;

[Circuits and Systems, IEEE Transactions on](#)

Volume 36, Issue 10, Oct. 1989 Page(s):1267 - 1274

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3. Making compaction-based parallelization affordable

Nakatani, T.; Ebcioğlu, K.;

[Parallel and Distributed Systems, IEEE Transactions on](#)

Volume 4, Issue 9, Sept. 1993 Page(s):1014 - 1029

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